Docket No.:

INTEL-0057

OEC 16 2005

PATENT TOW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

EXPEDITED PROCEDURE UNDER 37 C.F.R. § 1.116

Peter HAZUCHA, Gerhard SCHROM, Tanay KARNIK and

Vivek K. DE

Group Art Unit: 2816

Serial No.: 10/748,298

Filed: December 31, 2003

Examiner: L. Nguyen

Confirmation No.: 8425

Customer No.: 34610

For

TIMING CIRCUIT FOR SEPARATE POSITIVE AND NEGATIVE EDGE PLACEMENT IN A SWITCHING DC-

DC CONVERTER

U.S. Patent and Trademark Office Customer Window, Mail Stop **AF** Randolph Building 401 Dulany Street Alexandria, VA 22314

Dear Sir:

Transmitted herewith is an Amendment and/or Reply in the above identified application.

No additional fee is required.

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	38	41	0	x \$50.00 =	
Independent Claims	13	8	5	x \$200.00=	\$1,000.00
If multiple claims newly presented, add \$360.00 Fee for extension of time				d \$360.00	
		TOTAL FEE DUE			\$1,000.00

Please charge my Deposit Account No. <u>16-0607</u> in the amount of \$. An additional copy of this transmittal sheet is submitted herewith.

A check in the amount of $\frac{1,000.00}{1,000.00}$ (Check #16497) is attached.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 16-0607, including any filing fees under 37 C.F.R. 1.16 for presentation of extra claims and any patent application processing fees under 37 C.F.R. 1.17.

Respectfully submitted, FLESHNER & KIM, LLP

Mark L. Fleshner, Esq. Registration No. 34,596

Samuel W. Ntiros, Esq. Registration No. 39,318

P.O. Box 221200 Chantilly, VA 20153-1200 (703) 766-3701 MLF/SWN/Im Date: **December 16, 2005** Q:\Documents\2112-057\82495 Intel Docket No: P-18046 (Intel-0057)

Expedited Procedures Requested

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of Assignee: Intel Corporation) Customer No: 34610 HAZUCHA, P. et al. Confirmation No. 8425) Examiner: Nguyen, L. Serial No. 10/748,298 Filed: December 31, 2003 Group Art Unit: 2816

TIMING CIRCUIT FOR SEPARATE POSITIVE AND NEGATIVE EDGE For: PLACEMENT IN A SWITCHING DC-DC CONVERTER

AMENDMENT UNDER 37 CFR § 1.116

U.S. Patent and Trademark Office Customer Service Window, Mail Stop AF Randolph Building 401 Dulany Street Alexandria, Virginia 22314

December 16, 2005

Sir:

The following amendments and remarks are respectfully submitted in response to the Final Office Action mailed on October 27, 2005, in connection with the above-identified application.

Amendments to the Claims are in the Listing of Claims beginning on page 2.

Remarks begin on page 15.

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